Form 1449 (Modified)	Atty Docket No. NSC1P296/P05887	Application No.: NEW
Information Disclosure	Applicant:	
Statement By Applicant	Soon et al.	
	Filing Date	Group
(Use Several Sheets if Necessary)	HEREWITH	UNASSIGNED

U.S. Patent and Published Documents

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
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Foreign Patent or Published Foreign Patent Application

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Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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RW9	B2	EP0753891	1/15/97	EPO		->		
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Other Documents

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010	C2	Lai et al., Nordic Electronic Packaging Guideline, Chapter A, printed from: http://www.extra.ivf.se/mgl/A-WireBonding/ChapterA.htm , on March 16, 2004, 25 Pages
Dho	C3	Prof. Daniel F. Baldwin, "Fundamentals of IC Assembly," McGraw-Hill, Chapter. 9, pages 342-353, 2001
Q10	C4	Semiconductor Packaging Assembly Technology, printed from www.national.com, 8 pages, 1999

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ONO O	C5	Nakanishi et al., "Development of High Density Memory IC Package by Stacking IC Chips," Abstract No. XP000624986, IEEE, Vol. Conf. 45, Pages 634-640 (1995)		
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Examiner Rank & On Date Considered 1/11/05				

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.